

REMARKS

Claims 1-5 are currently pending in the present application.

Rejection under 35 U.S.C. § 102

Claims 1-20 were rejected under 35 U.S.C. § 102(a) as being anticipated by *Chow et al.* (PipeRench Implementation of the Instruction Path Coprocessor, IEEE, Dec. 2000). Applicants respectfully traverse such rejection.

Enclosed is an affidavit from Applicants to swear behind *Chow*. The affidavit attests to the fact that the submission date of the invention disclosure for the present application is earlier than the publication date of *Chow*. The submission of the affidavit is considered timely because Applicants had made an offer to submit such an affidavit in the previous response. Because the invention disclosure of the present application pre-dates *Chow*, the § 102 rejection is believed to be overcome.

CONCLUSION

Claims 1-5 are currently pending in the present application. For the reasons stated above, Applicants believe that independent Claim 1 along with its dependent claims are in condition for allowance. The remaining prior art cited by the Examiner but not relied upon has been reviewed and is not believed to show or suggest the claimed invention.

No fee or extension of time is believed to be necessary; however, in the event that any fee or extension of time is required for the prosecution of this application, please charge it against IBM Deposit Account No. **09-0456**.

Respectfully submitted,



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